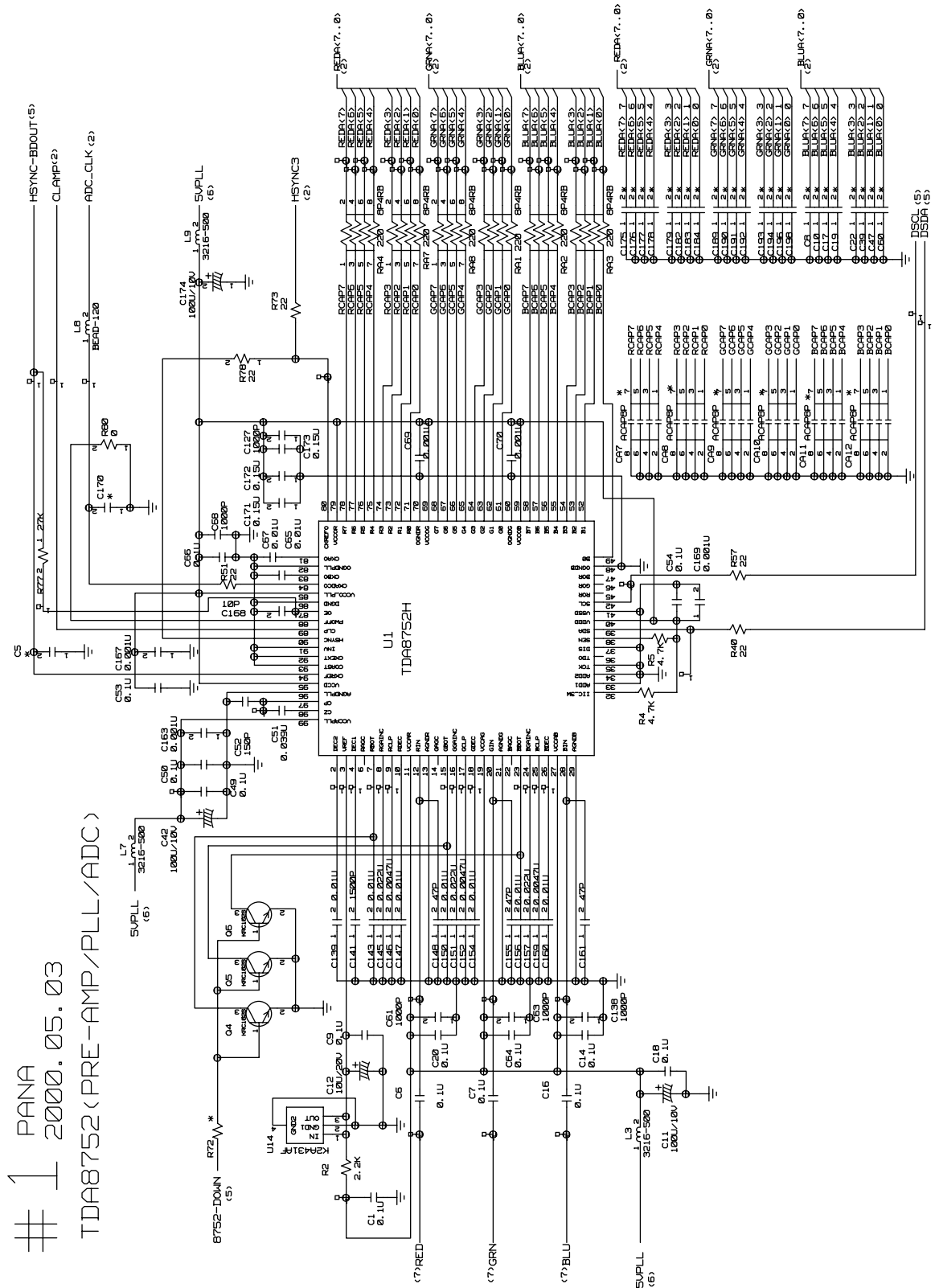
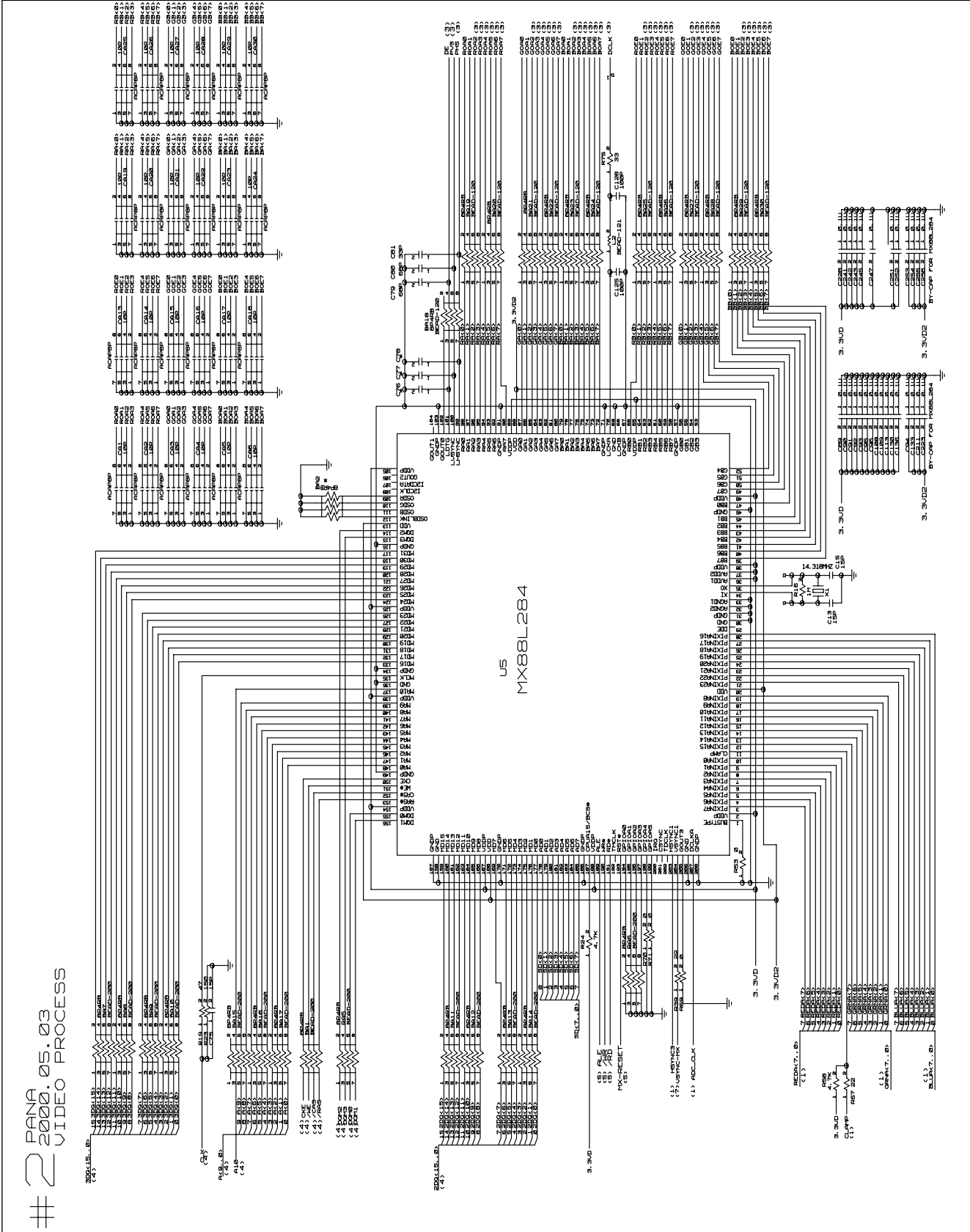


# SCHEMATIC DIAGRAM

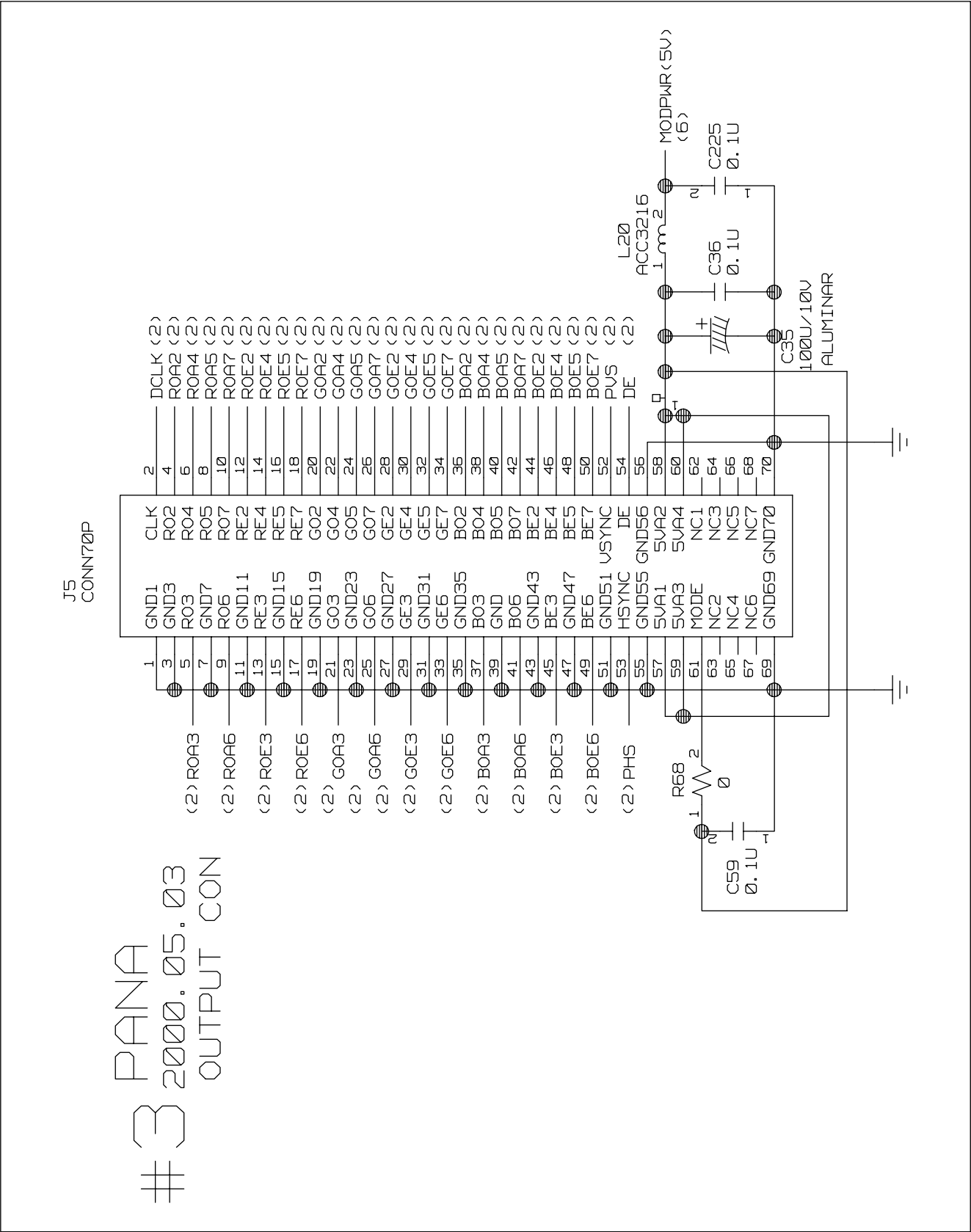
## 1. TDA8752 (PRE-AMP/PLL/ADC)



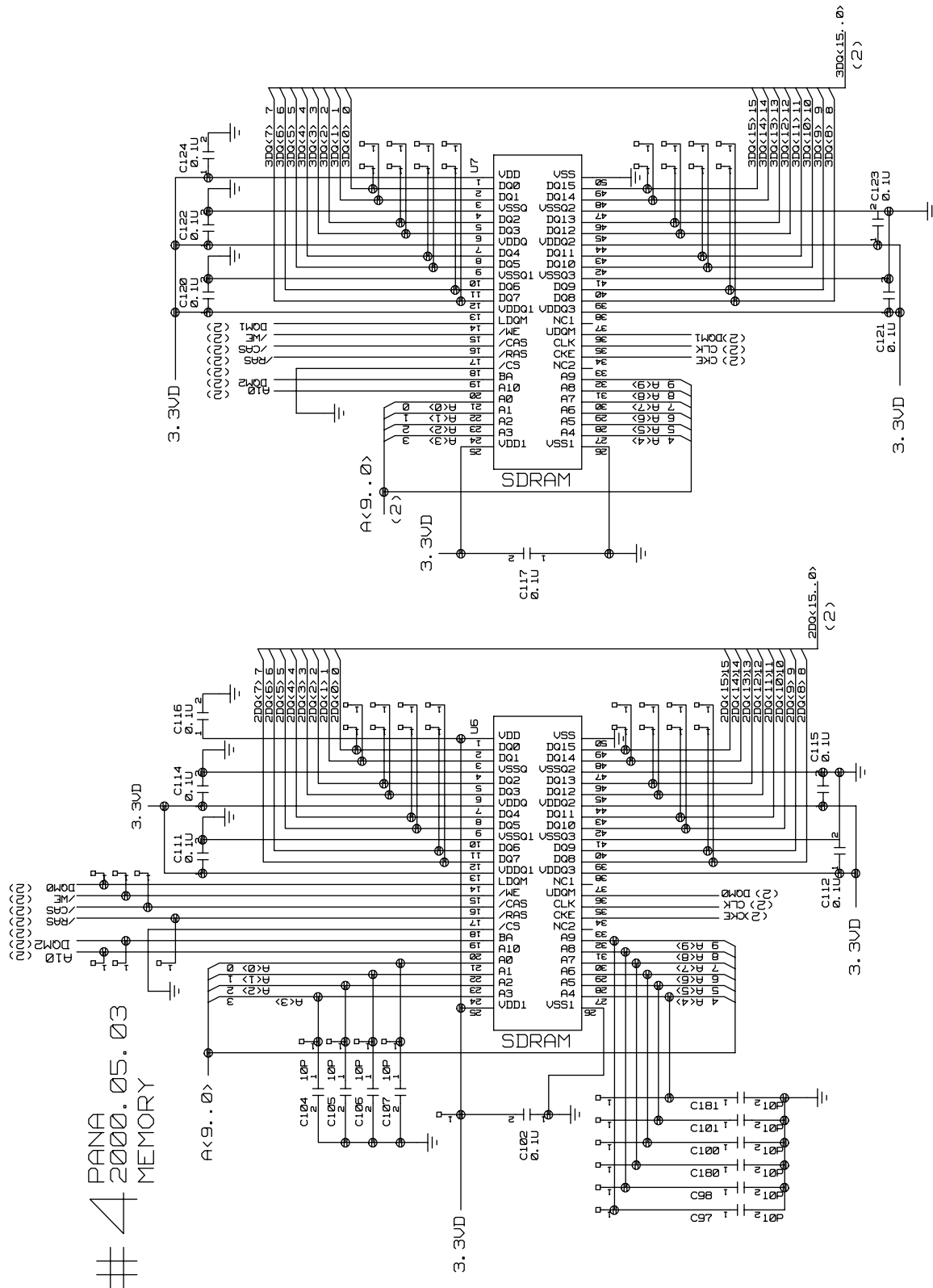
## 2. VIDEO PROCESS



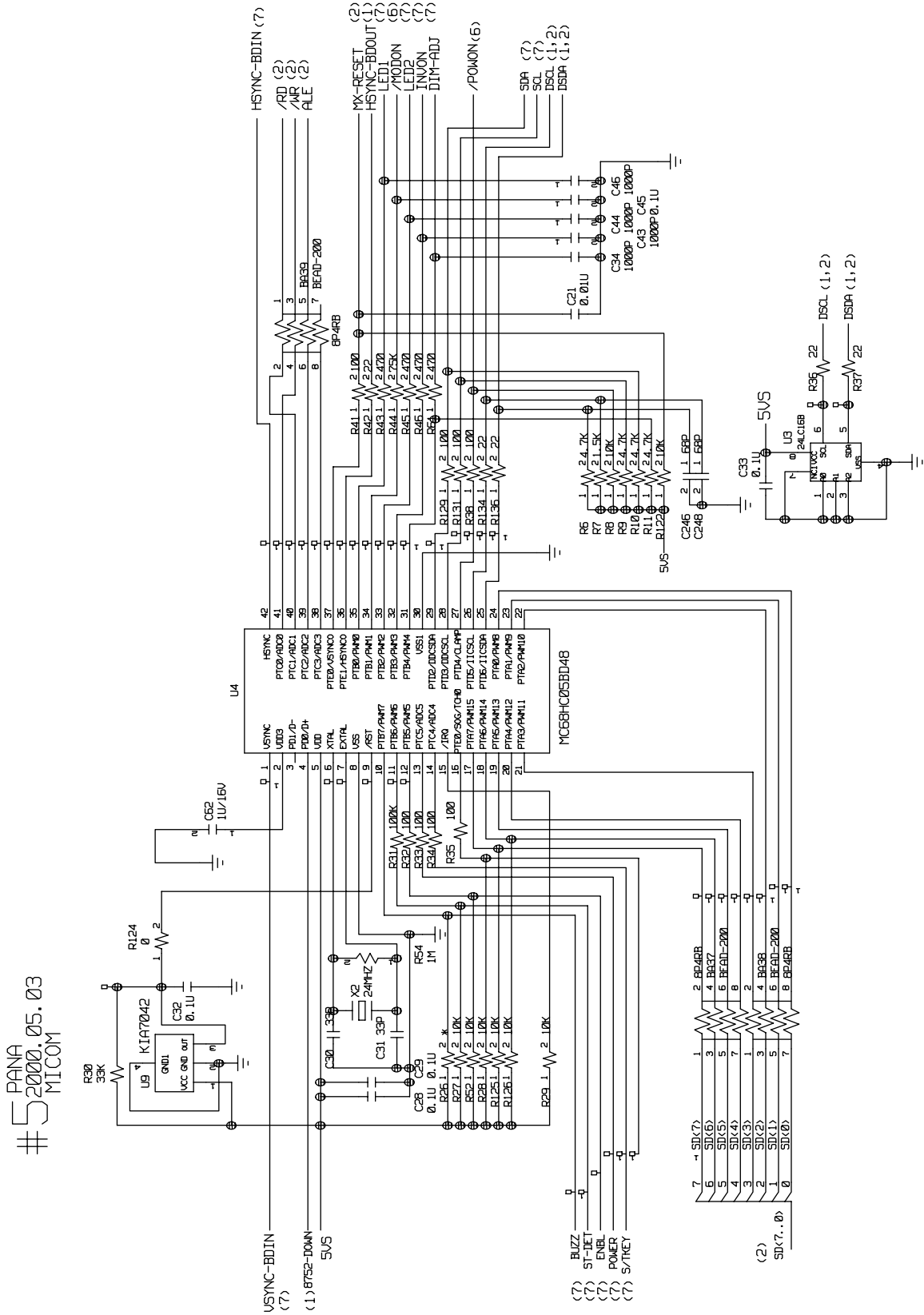
### 3. OUTPUT CON.



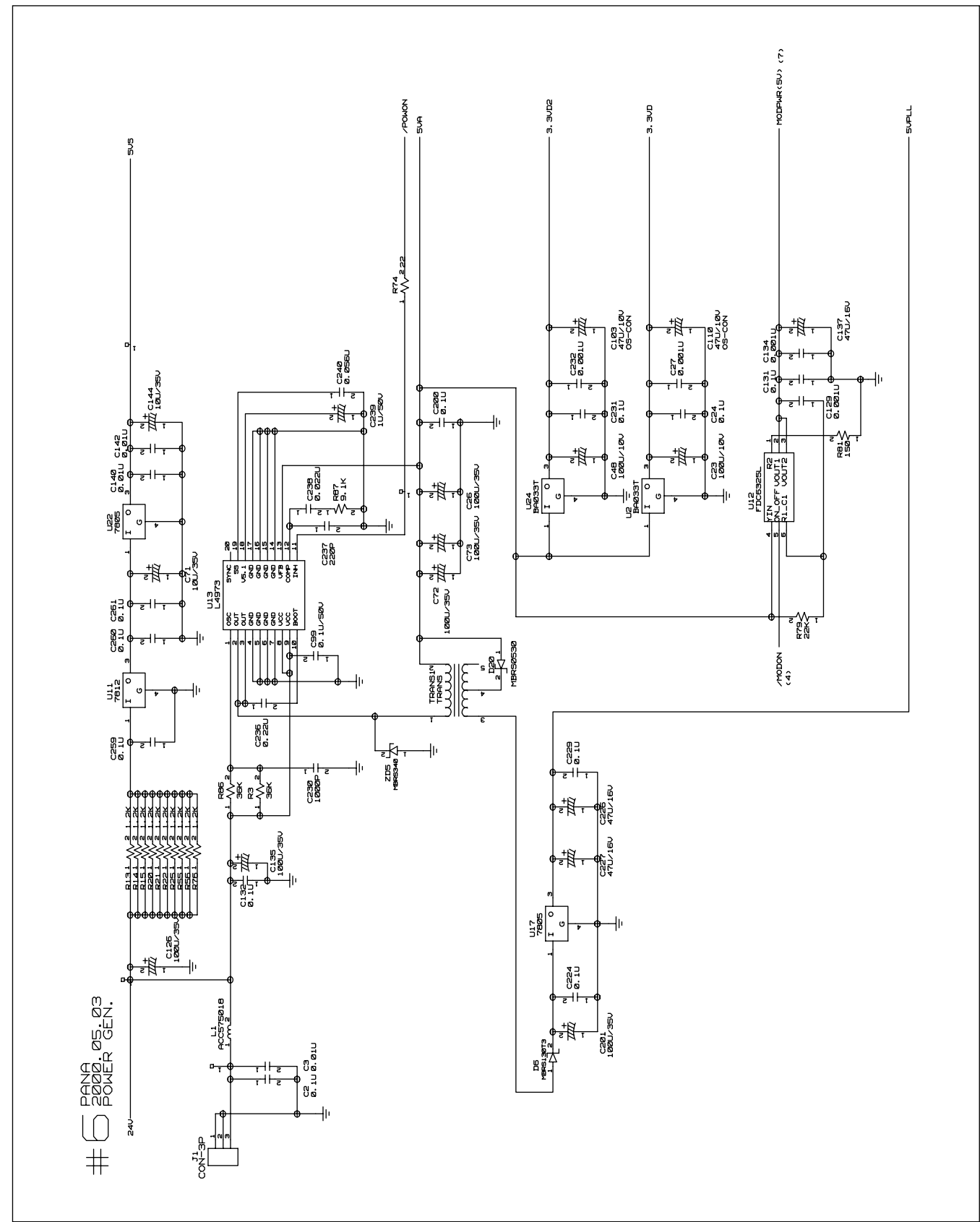
## 4. MEMORY



## 5. MICOM



## 6. POWER GEN.



## 7. CONNECTOR & JACKS

#7 PANA  
2000.05.03  
CONNECTOR & JACKS

