

Block Diagram of VSC main Block Diagram of STB main IC SPEC





RT-BA10 STB Main Block Diagram



VPC 3230 Comb Filter Video Processor

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Description

The VPC3230xD/324xD is a high quality, single-chip video front-end, which is targeted for 4:3 and 16:9, 50/60 and 100/120Hz TV sets. It can be combined with other members of the DIGIT3000 IC family (such as DDP 33x0A/B, TPU 3040) and/or it can be used with 3rd-party products.

Features

Four CVBS, one S-VHS input, one CVBS output

Two RGB/YCrCb component inputs, one Fast Blank(FB) input

High-performance adaptive 4H comb filter Y/C separator with adjustable vertical peaking

Multi-standard color decoder PAL/NTSC/SECAM including all substandards

One 20.25MHz crystal, few external components

6V supply voltage

SDA9410 Display Processor & Scan Rate Converter

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<u>SDA9410</u>

- Display Processor & Scan Rate Converter Features
- Digital interlaced Y/C input,
- progressive analog Y/U/V output
- High performance scan rate converter
- 4:2:2 luminance and chrominance
- parallel (2x8 wires)
- Scan rate conversion
- Motion compensated 100/120 Hz interlaced scan conversion
- Motion compensated 50/60 Hz progressive scan conversion
- **D**/A converters
- 9 bit amplitude resolution for Y, -(R-Y), -(B-Y) output
- 3.3V + 5% supply voltage



74F541

3-state Buffer

Description

The 74F541 are octal buffers that are ideal for driving bus lines or buffer memory address registers. The outputs are capable of sinking 64mA and sourcing up to 15mA, producing very good capacitive drive characteristics. The devices feature input and outputs on opposite sides of the package to facilitate printed circuit board layout.

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Features

- High impedance NPN base inputs for reduced loading (20uA in High and Low states)
 - Octal bus interface
 - 3-state buffer outputs sink 64mA

15mA source current

THS8083 Video& Graphics Digitizer with Digital PLL





Description

THS8083 is a complete solution for the digitalizing of video & graphic signals in RGB or YUV/YCbCr color spaces. The device supports pixel rates up to 80 MHz to 95 MHz, depending on the speedgrade of the device

Features

Three digitalizing channels, each with independently controllable Clamp, PGA and ADC

ADC: 8 bit 80MSPS

Support for 4:4:2 and 4:2:2 (ITU.BT-601 style output modes to reduce board traces and video ASIC'S

Fully integrates digital PLL(including loop filter) for pixel clock generation

3.3V supply voltage

CXA2101AQ Multi-Component Processor (Base Band Video Signal Processor IC)

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Description

The CXA2101AQ is a bipolar IC which integrates the following functions on a single chip; base band signal processing, RGB signal processing , and video switching for 4 systems (including HV sync signal processing) using Y/Cb/Cr inputs.

It was developed for multiscan TVs, and enables high-end TV systems to be configured.

Features

On-chip video switching for 4 systems

Y/Cb/Cr input for one system, external Y/Cb/Cr input for 1 system

Analog RGB input for 2 systems

Normal, PAL-FF, HD-TV supported

9V supply voltage

MX88L284 Scan Converter

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Description

The MX88L284 is a highly integration chip for Flat Panel Display application. With Macronix's Smartscaling – 2 filter, it provides high quality scaled video image and format conversion.

General features

Converts NTSC/PAL and PC video signal into flat panel display device timing and resolution.



Built-in OSD generator with 64 ROM fonts, and 64 programmable RAM fonts.

Support configurable SDRAM/SGRAM(x0 x1 and x2) for different resolution to minimize the system cost.

Input

PC Video up to 1024x768 @ 85Hz operation mode

Support YCrCb422, RGB888 mode (interlaced and Non-interlaced)

Support input H/V sync. Polarity and odd/even field detection

Support digital input capability

Support SDRAM/SGRAM x0x1 and x2 configuration

3.3V power supplier

Output

Support following resolution and frequency

Resolution	800x600 (SD)	1024x768	1280x1024
Horizontal frequency	22~55	20~70	64
Vertical frequency (Hz)	50~75	50~75	60
Dot clock (MHz)	32.5~60	25~80	108

Single (18/24) and Dual (36/48) bit RGB data output

Support OSD MUX capability for On-Screen-Display chip input

Built-in OSD generator

CPU Interface

Support direct 8 bit uP interface and serial bus (high-speed) interface

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SDRAM-KM416S1020C

	r		-	1	
VDD	4	10	50	Ь	Vss
DQO	Ч	2	49	Ь	DQ15
DQ1	Р	3	48	Ь	DQ14
Vssq	d	4	47	Ь	Vssq
DQ2	d	5	46	Ь	DQ13
DQ3	d	6	45	Ь	DQ12
VDDQ	d	7	44	Ь	VDDQ
DQ4	d	8	43	Ь	DQ11
DQ5	d	9	42	Ь	DQ10
Vssq	d	10	41	Ь	Vssq
DQ6	d	11	40	b	DQ9
DQ7	d	12	39	Ь	DQ8
VDDQ	d	13	38	b	VDDQ
LDQM	d	14	37	b	N.C/RFU
WE	d	15	36	þ	UDQM
CAS	d	16	35	þ	CLK
RAS	q	17	34	þ	CKE
CS	q	18	33	þ	N.C
BA	q	19	32	þ	A9
A10/AP	q	20	31	þ	A8
A0	4	21	30	þ	A7
A1	q	22	29	þ	A6
A2		23	28	þ	A5
A3	q	24	27	þ	A4
VDD	q	25	26	þ	Vss
				1	

Description

The KM416S1021C is 16,777,216 bits synchronous high data rate Dynamic RAM organized as 2 x 524,288 words by 16 bits, fabricated with high performance CMOS technology. Synchronous design allows precise cycle control with the use of system clock I/O transactions are possible on every clock cycle. Range of operating frequencies, programmable burst length and programmable latencies allow the same device to be useful for a variety of high bandwidth, high performance memory system applications.

Features

JEDEC standard 3.3V power supply

SSTL_3 (Class II) compatible with multiplexed address

Dual banks operation

MRS cycle with address key programs

- CAS latency (2 & 3)

- Burst length (1, 2, 4, 8 & Full page)

- Burst type (Sequential & Interleave)

All inputs are sampled at the positive going edge of the system clock.

Burst read single-bit write operation

Auto & self refresh

64ms refresh period (4K cycle)

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CXA2022 **I²C Bus Sound Processor for TV**

LIN

MVFO

PS3B

CCL



Description

The CXA2022S is a bipolar IC designed as an IC bus control sound processor for TV. This IC has simulate stereo, surround, tone control, balance, volume, muting, AGC and other functions.

Features

Allows control I²c bus

Employs a special surround system to B LOUT prevent "vocal missing" in the surround 🕲 ps3a mode

> Adopts an AGC circuit to absorb the difference in sound level between input sources and improves S/N ratio of hearing characteristics

14V supply voltage

2-channel 10w AF Power Amplifier for Use in Home Stereo, TV Applications

LA4282

Description

The LA4282 is an IC which seals a highoutput power amplifier for TVs and monitors in a compact package.

Features

High-power 2-channel AF power amplifier

Low pop noise at the time if power supply **ON/OFF**

Good ripple rejection (58 db typ)

External muting available

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<u>CXA2069</u>

S2-Compatible 7-Input 3-Output Audio/Video Switch



Description

The CXA2069Q is a 7-Input 3-Output Audio/Video Switch featuring I2C bus compatibility for TVs. This IC has input pins that are compatible with S2 protocol.

Features

7 inputs 3 outputs

4 inputs that are compatible with S2 protocol

Wide band video amplifier (20 MHz,-3 dB)

Y/C MIX circuit

Serial control with I2C bus

High impedance maintained by $\mathrm{I}^2\mathrm{C}$ bus lines (SDA, SCL) even when power id OFF

12V Supply voltage

	d de Contra			
V _{i,SIF1}	1	\bigcirc	30	V _{i,SIF2}
V _{i,SIF1}	2		29	V _{i,SIF2}
Vsw	3		28	R _{comp}
GND	4		27	V _{o,AM}
C _{AGC}	5		26	V _{o,FM}
V _{i,VIF1}	6		25	Vs
V _{i,VIF1}	7		24	V _{AFC}
CAGC	8		23	V _{vco}
GND	9		22	V _{vco}
V _{i,VIF2}	10		21	Vsw
V _{i,VIF2}	11		20	LF
R _{top}	12		19	C _{Ref}
I _{tun}	13		18	GND
V _{o,vid}	14		17	C _{BL}
(stand.)	15	94 8680	16	V _{SW} (L

<u>TDA4474</u> Multistandard Video-IF

and Quasi Parallel Sound Processing

Description

The TDA4474 is an integrated bipolar circuit for full multistandard video/sound IF(VIF/SIF) signal processing in TV/VCR and multimedia applications. The circuit processes all TV video IF signals with negative modulation (e.g. B/G standard), positive modulation(e.g. L standard) and the AM, FM/NICAM sound IF signals.

Features

Four IF inputs (2xVIFin and 2xSIFin)

Very linear video demodulation, good pulse response and excellent inter modulation figures

VIF-AGC for negative modulated signals(peak sync. detection) and for positive modulation(peak white/black level detector)

Alignment-free quasi parallel sound(QPS) mixer for FM/NICAM sound IF signals

5V supply voltage; low power consumption

16/40

Vsw

MSP 3411G Multistandard Sound Processor with Virtual Dolby Surround



Description

The MSP 34x1G family of single-chip Multistandard Sound Processors covers the sound processing of all analog TV-Standards worldwide, as well as the NICAM digital sound standards. The full TV sound processing, starting with analog sound IF signal-in, down to processed analog AF-out, is performed on a single chip.

The MSP4x1G has all functions of the MSP3401G with the addition of a virtual surround sound feature

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<u>MSP 3411G</u> Multistandard Sound Processor with Virtual Dolby Surround Features

Four Stereo SCART (line) inputs,

one Mono input; two Stereo SCART output

Complete SCART in/out switching matrix

Standard Selection with single I²C transmission

Two selectable sound IF (SIF) inputs

Automatic Sound Selection (mono/stereo/bilingual), new registers MODUS, STATUS

3D-panorama virtualizer (approved by Dolby Laboratories) with noise generator

AVC: Automatic Volume Correction

Demodulation of the FM-Radio multiplex signal

8V power supply



Input selector switch for high definition displays

BA7657F

Description

The BA7657F is for high definition displays, and have internal switches for switching between broadband RGB signals and HD/VD signals, as well as an internal synchronization separator. These ICS simplify the designing of input units for deluxe displays

Features

Internal broadband RGB switch (frequency characteristics : 250MHz, -3dB)

Internal HD/VD switch

Internal synchronization separator for synchronizing signals superimposed onto G signals.

5V power supply

Connection Diagram of MT-40PA10



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Pin Detail of MT-40PA10





number



Connection Diagram of RT-BA10



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Pin Detail_of RT-BA10

STB-CONTROL B/D 12PIN



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Trouble Shooting Guide

X - Board COF Connector separation Lift up the right and left of X-BOARD CONNECTOR.



Lift up X-BOARD CONNECTOR and separate COP CONNECTOR by pulling up.

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When you handle COF CONNECTOR, don't pressure. First release LOCK and separate. COF Connector If COF CONNECTOR is damaged you should replace MODULE ASS'Y. so be aware of this::





When you exchange X-Board, first you should separate COF Connector. Be careful not to damage it. COF Connector is attached to Module. Since COF Connector is torn, Module Ass'y must be replaced a new one. Be careful!

X - Board Connector separation

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Lift up each edge of left/right.



Lifted condition

Be careful to handle LOCK or it can be hurt. When LOCK is hurt, replace a new X-BOARD.



It's easy to separate it by releasing Connector Lock .

Do not pressure. Be careful to handle LOCK or it can be hurt. When LOCK is hurt, replace a new X-BOARD



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Pull the white LOCK as shown in arrow





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Pull the white LOCK as shown in arrow.

Separate COF CONNECTOR by pulling in the left.

warnign

Be careful to handle LOCK part and COF Connector

when LOCK part is damaged, you should replace a new Y-Board. In case of COF Connector, Module Ass'y

Z - Board COF Connector separation



Separate the fixed Screw of Z-Board... Pull out Lock as shown in arrow. warning

COF Connector

Condition in Lock part is pulled

be careful not to tear COF Connector. If COF Connector is torn, replace a new Module Ass'y. **Pull COF** Connector as shown in arrow. It's easy to separate COF on condition that Z-Board Screw is separated. In case Z-Board is assembled, it's really hard to separate.

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each Connector separation

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Push LOCK and pull out











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Controller Board VSC Board Connector





Gas injection (sealing up) condition





Be sealed up after gas injection

Be sealed up after gas injection

warning

Be careful to handle the sealed-up part after gas injection.

If it is broken the gas escapes. So replace a module.

Power is on and off 2 ~ 3 minutes later (Protect)



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As soon as the Power is On, it's off in 2 ~ 3 minutes. (Protect)



Inferior phase : as soon as the power, it's off in 2 -3minutes.

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(PROTECT circuit operation) inferior cause : no VS voltage L813 Coil dry joint.

Check

Open the Connector connecting to each Board to check the power is off. if each Board is same, check the Power Board and voltage.

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HEATRUN : WHITE



PD-40X2



Press the ADJ KEY and check the position of add bar by changing WHITE or RED or BLUE or GREEN

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PD-40X2 uses 4 board such as left,right, top and bottom. Divide the screen in 5 and once you see ADD BAR check COF CONNECTOR between MODULE and X-BOARD. If there is no defect in COF CONNECTOR replace X-BOARD. But the problem still remains and check the connector between X-BOARD and CONTROL BOARD. And if you can't find defect, check CONTROL.

PD-60X3 uses 2 X-BOARDs such as top and bottom so when the problems occur in top or bottom, check ADD BAR first and then CONNECTOR, X-BOARD, CONNECTOR and CONTROL BOARD



1.June 13 inferior receipt PD 40X 2 Filed inferior good Set No : 912KC 00036 Module No : P239102815-215 customer : in kyeongi province contents: tape droop management: tape rework



13 inferior receipt PDP 40 NVDN 4 Set No : 104KC 00176 Module No : 503007 PRODUCT RECORD : DND sends it to C/S after reworking POWER B/D .

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contents : B color Address Open 1line causes : dented COF : no inferior phase in DND process, assumed that inferior happens during the analysis in DND ,due to Power B/D

COF dented



inferior receipt PDP 40NVDN 4 Filed FLATRON Plasma Set No : 104KC 00206 Module No : 32184 customer : a surgeon in Kyeongi province contents : Address R color inferior cause : DATA output inferior by X-L-TOP IC14,16 pin dry joint (normal curve by tearing off IC Pin)

management : X-L-TOP replacement





Dry joint



Inferior phase management causes The power board PCB replacement of SMPS Under 14V 15V Line. **TRANS** replacement electric discharge inferior (B,D Type) occurs and the power's off because protect circuit is **X-Board replacement** off. -. The power board SMPS not winded enough has inferior X-Board the electric discharge inferior because of the short of the voltage capacity. The blue spreads on the screen (electric discharge inferior) and the power's off in 2 --. X-Board TR Short, 3 minutes this phase repeats whenever the over-electrical load power is on. Protect circuit.

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Check

Measure SMPS 2차 측 15V Line by Digital Multi Meter and check the voltage changes. If it is less than 14V, the screen ins broken with the blue. As soon as the power is ON and it's off showing this kind of screen. Remove all connectors and check the board not off. . X-Board inferior and the power board SMPS replacement.

FLATRON Plasma **Inferior phase** cause management The power board under 14V 15V Line. **PCB** replacement electric discharge or SMPS TRANS inferior occurs. replacement (B,D Type) -. The power board SMPS is not winded enough. because the voltage capacity is short ,electric discharge inferior occurs. The whole screen is broken with the blue (electric discharge inferior) whenever the power is on this phase occurs. OSD is normal.

Check

Measure SMPS 2 15V Line by Digital Multi Meter and check the voltage changes. If it is less than 14V, the screen ins broken with the blue. (electric discharge inferior)

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Inferior phase	causes	management
	Presume X-BOARD inferior. cause : X-BOARD TOP RIGHT 5V doesn't turn	X-BOARD TOP RIGHT 5V adoesn't turn
The top left part of screen is broken (X-BOARD TOP RIGHT)		
Check X-BOARD TOP RIGHT 5V CHECK SMPS X-BOARD TOP RIGHT 5V CHECK	37/40	C102 C103 C103 C103 C104 C103 C104 C104 C104 C104 C104 C104 C104 C104

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Inferior phase	causes	management
	Presume X-BOARD inferior. cause : when X-BOARD TOP RIGHT VA(70V) doesn't turn, this phase occurs.	X-BOARD TOP RIGHTVA(70V) doesn't turn. VA(70V) LINE OPEN.
The pink in the top left part of the screen. (X-BOARD TOP RIGHT)		
Check X-BOARD TOP RIGHT VA (70V) CHECK SMPS X-BOARD TOP RIGHT VA (70V) CHEC		C192 C193 C194 C192 C193 C194 C192 C193 C194 C193 C194 C193 C194 C193 C194 C193 C194 C193 C194 C193 C194 C194 C194 C194 C194 C194 C194 C194 C194 C194 C194 C194 C194 C194

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5 tust			
Inferior phase	Causes	management	
	X-BOARD TOP RIGHT 12V doesn't turn Board VA(70V) doesn't turn.	X-BOARD TOP RIGHT 12V LINE OPEN X-Board Top Right VA(70V) Line Open	
the 3/5 top left in the screen isn't out (X-BOARD TOP RIGHT)			
Check X-BOARD TOP RIGHT 12V CHECK (in 0V,this	phase.)	C102 C103 C123 C102 C103 C123 C103 C123 C10	

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X-Board Top Right VA(70V) CHECK. (in 0V this phase)

SMPS --- X-BOARD TOP RIGHT supplied 12V ,VA(70V) CHECK.



FLATRON Plasma **Inferior phase** management causes **X-BOARD TOP LEFT** X-BOARD TOP LEFT **12V LINE OPEN.** 12V doesn't turn. **X-BOARD TOP LEFT** X-BOARD TOP LEFT VA(70V) LINE OPEN VA(70V) doesn't turn. The 3/5 top right of the screen isn't out. (X-BOARD TOP LEFT) Check X-BOARD TOP LEFT 12V CHECK (in 0V)

X-BOARD TOP LEFT VA(70V) CHECK(IN 0V)

SMPS --- X-BOARD TOP LEFT , 12V,VA(70V) CHECK.